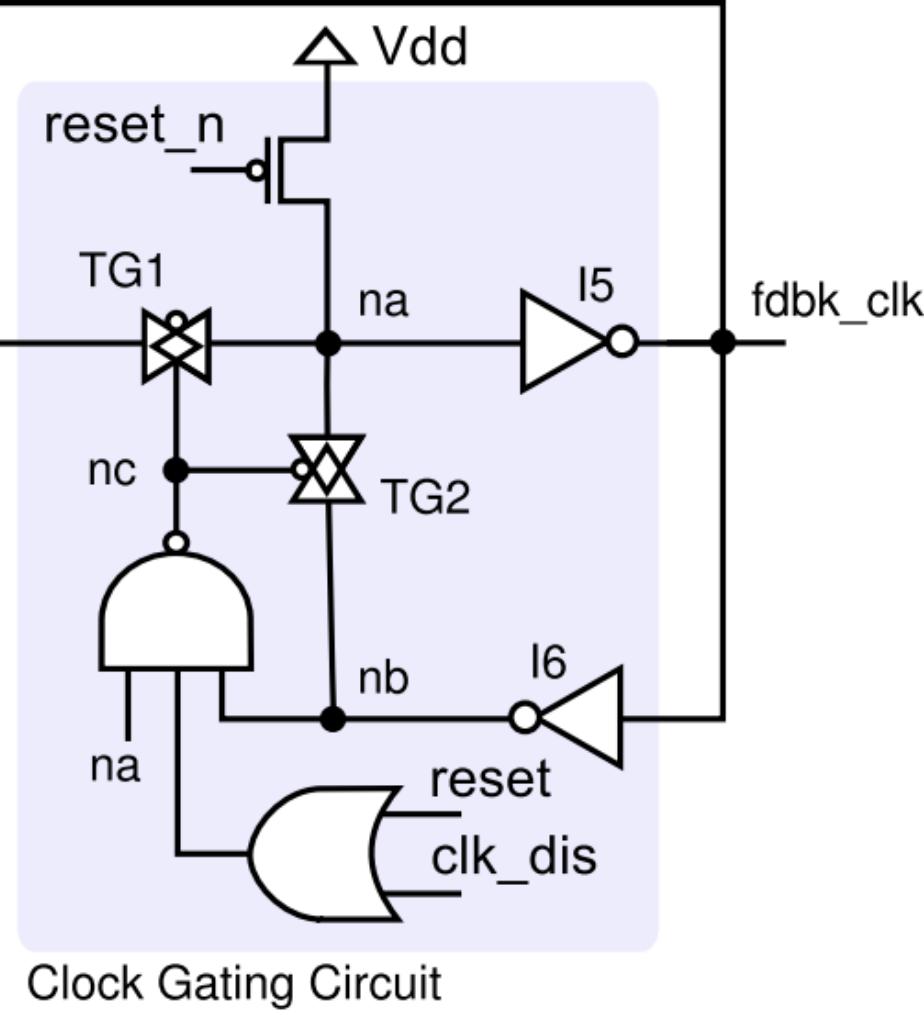
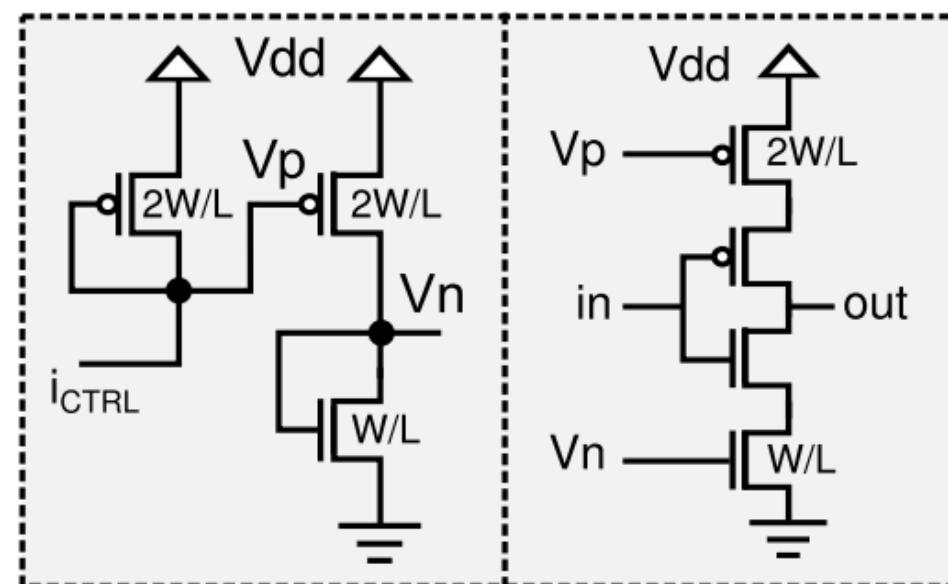


Current Mirror



Clock Gating Circuit