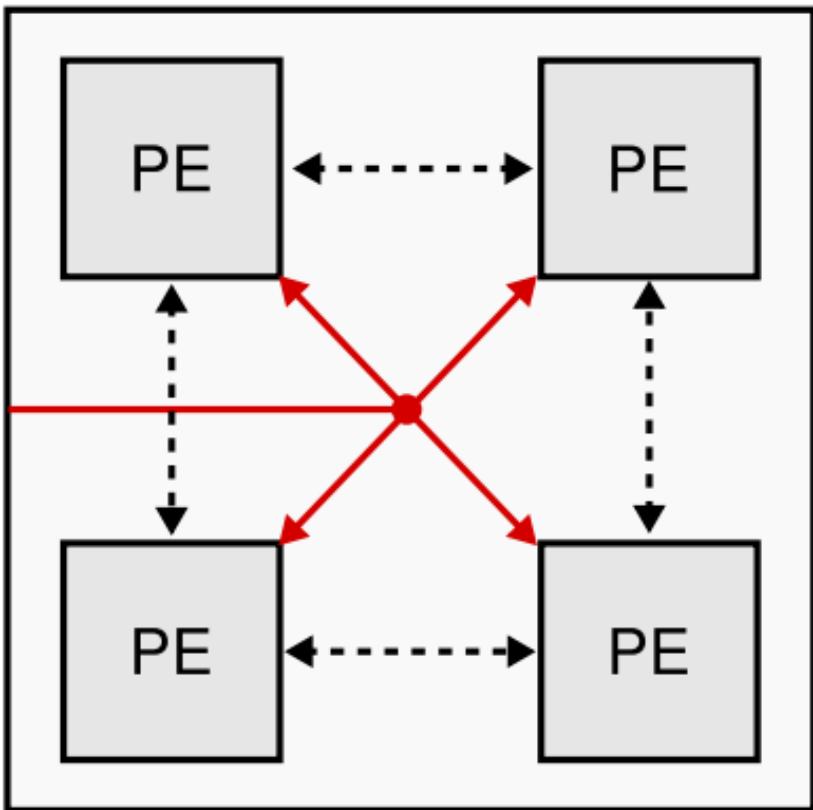


# GALS MPSoC

External Reference Frequency  
ref\_clk

External I/O



→ Reference clock → Data exchange path

## Processing Element (PE)

