



ASYNCR2016

PORTO ALEGRE BRAZIL

May 8 to 11 2016

INDUSTRIAL PAPERS and FRESH IDEAS Workshop
CALL FOR PAPERS

22nd IEEE International Symposium on Asynchronous Circuits and Systems

A large number of VLSI chips and digital systems designs contain multiple, interacting timing domains. The resulting asynchrony is both an opportunity and a challenge for design, validation, and test. The International Symposium on Asynchronous Circuits and Systems (ASYNC) is the premier forum for researchers to present their latest findings in the area of asynchronous design.

INDUSTRIAL PAPERS

ASYNC 2016 will include a special industrial workshop with papers and tutorials from industry and research on the state-of-the-art application of asynchronous designs to both existing and emerging technologies. The topics are targeted at industry and include:

- Synchronizers and clock domain crossing techniques
- Techniques for combining asynchronous and clocked designs
- CAD tools to integrate asynchronous circuits with clocked designs
- Circuit designs, case studies, comparisons, and applications

We solicit 1-to-2-page submissions for the workshop, IEEE double-column conference format. These papers will go through a separate light-weight review process. Accepted papers will be published in the IEEE digital library IEEEXplore and symposium proceedings.

“Fresh Ideas” Workshop / Tools & Demos

ASYNC 2016 will include a special workshop to present “fresh ideas” in asynchronous design, not yet ready for publication. We solicit 1-to-2-page submissions for the workshop, which will go through a separate light-weight review process. Accepted submissions will be assembled and handed out at the workshop. We also solicit tools and demos for presentation at the conference.

Important Dates

Industrial Paper
deadline: 29 Feb 2016

Publication-ready
final version : 6 Mar 2016

Fresh Idea Workshop
deadline: 7 March 2016

General Chair
Ney Calazans, PUCRS, Brazil
ney.calazans@pucrs.br

Program Chairs
Peter Beerel
Julian Pontes

Invited Talks and Keynotes Chair
Sérgio Bampi, UFRGS, Brazil

Local Arrangements Chair:
Fernando Moraes

Finance Chair:
Don Draper

Publication Chair:
Edith Beigné, CEA-LETI, France
Erik Brunvand, Un. of Utah, USA
Alexandre M. Amory, PUCRS, Brazil

Industrial Liaison Chair
Prasad P. Joshi, Intel, USA

Publicity Chair
Ricardo Reis, UFRGS, Brazil

www.asynctsymposium.org

